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Distributed-directory scheme: scalable coherent interface

[James, D.V.](#) [Laundrie, A.T.](#) [Gjessing, S.](#) [Sohi, G.S.](#)
Apple Comput., New York, NY, USA;

This paper appears in: **Computer**
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Volume: 23 , Issue: 6
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ISSN: 0018-9162
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Abstract

The scalable coherent interface (SCI), a local or extended computer backplane interface being defined as an IEEE standard project (P1596), is discussed. The interconnection is scalable, meaning that up to 64 processor, memory, or I/O nodes can effectively interface to a shared SCI interconnection. The SCI structures are described, and sharing-list addition and removal are examined. Optimizations being considered to improve the performance of large system configurations are discussed. Request combining, a use of linked-list coherence, is described. SCI's optional extensions, including synchronization using a queue lock bit, are considered.

Index Terms

Inspe

Controlled Indexing

[computer interfaces](#) [data structures](#)

Non-controlled Indexing

[IEEE standard project P1596](#) [SCI sharing-list structures](#) [computer backplane interface](#)
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